THE COMMISSIONER OF PATENTS AND TRADEMARKS Washington, D.C.

· Sir:

Ror:

Fansmitted herewith for filing is the Patent Application of:

A LOCK DETECTOR CIRCUIT FOR DEJITTER PHASE LOCK LOOP (PLL) APPLICATIONS

Enclosed are:					
$\overline{\mathbf{x}}$ $\overline{7}$ sheets of drawing(s) - formal.					
X An assignment of the invention to EPSON Research and Development, Inc.					
An associate power of attorney			Applicant claims small entity status		
Request	& Certification	n under 35 US	C 122(b)(2)(b)(:	i)	
The filing fee	e has been calc	ulated as sho	wn below:		
	(Col. 1)	(Col. 2)	OTHER THAN A	SMALL ENTITY	
FOR:	NO. FILED	NO. EXTRA	RATE	FEE	
Dasic fee		><		\$ 740.	
TOTAL CLAIMS	56 -20=	36	x 18 =	\$ 648.	
ander claims	12-3=	9	x 84 =	\$ 756.	
MULTIPLE DEPENDENT CLAIM PRESENTED + 260 =					
		5	SUB TOTAL	\$ 2,144.	
The state of the s			ASSIGNMENT	\$40.	
T			FOTAL	\$ 2,184.	
Please charge my Deposit Account No. 19-0033 in the amount of \$ 2/84, A duplicate					
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X Any additional filing fees required under 37 CFR \$1.16.					
X Any patent application processing fees under 37 CFR §1.17.					
Respectfully submitted,					
STEPHEN B. ACKERMAN, REG. NO. 37,761					
Express Mail Certificate					
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I hereby certify that this correspondence is being deposited with the United States Postal Service as Express					
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